

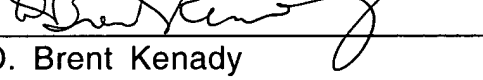
REMARKS

Applicant has cancelled claims 1-40. Applicant hereby adds new claims 41-61 for full consideration by the Examiner.

The Examiner is requested to phone the undersigned if the Examiner believes such would facilitate prosecution of the present application. The undersigned is available for telephone consultation at any time during normal business hours (Pacific Time Zone).

Respectfully submitted,

Dated: 2-5-02

By: 
D. Brent Kenady
Reg. No. 40,045

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/228,705
Priority Filing Date January 12, 1999
Inventor Joseph M. Brand
Assignee Micron Technology, Inc.
Priority Group Art Unit 2826
Priority Examiner Alexander O. Williams
Attorney's Docket No. MI22-1939
Title: Integrated Circuit Device (As Amended)

VERSION WITH MARKINGS TO SHOW CHANGES MADE ACCOMPANYING
PRELIMINARY AMENDMENT

In the Specification

At page 1, before the "Technical Field" insert the following:

RELATED PATENT DATA

This patent resulted from a continuation application of U.S. Patent Application Serial No. 09/228,705, filed January 12, 1999, entitled "Integrated Circuit Device, Synchronous-Link Dynamic Random Access Memory Device", naming Joseph M. Brand as inventor, which is a divisional application of U.S. Patent Number 6,008,074 issued December 28, 1999, entitled "Integrated Circuit Device, Synchronous-Link Dynamic Random Access Memory Device, Method of Forming An Integrated Circuit Device and Method of Forming a Synchronous-Link Dynamic Random Access Memory Edge-Mounted Device", naming Joseph M. Brand as inventor, the disclosure of which is incorporated by reference.

In the Claims

The claims have been amended as follows. Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

Claims 1-40 have been cancelled without prejudice. There are no amendments to the claims.

-END OF DOCUMENT-